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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of	3	Attorney Docket Number	MIC-05632-5D
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U.S. PATENT DOCUMENTS

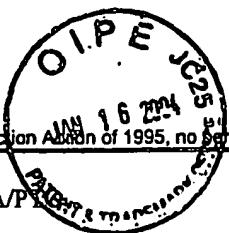
Examiner Initials*	Cited No. ¹	U.S. Patent Document Number		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear
		Kind Code ² (if known)				
QV	A01	4,336,613		Hewes	6/22/1982	
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	A03	5,012,490		Myer	4/30/1991	
	A04	5,020,147		Okanobu	5/28/1991	
	A05	5,077,835		Margairaz et al.	12/31/1991	
	A06	5,134,406		Orr	7/28/1992	
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EXAMINER /Quochien Vuong/	DATE CONSIDERED	09/08/2006
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cited No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines Where Relevant Info. Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
QV	A24	WO	98/49779			11/5/98		
QV	A25	WO	99/59256			11/11/99		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

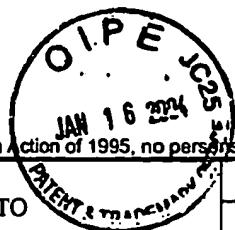
QV	PCT Written Opinion for International Application No. PCT/US/99/09995, filed May 6, 1999.
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	Chang, P. J., Rofougaran, A., and Abidi, A. A., "A CMOS Channel-Select Filter for a Direct-Conversion Wireless Receiver," <i>Symp. On VLSI Circuits</i> , Honolulu, pp. 62-63, 1996.
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

QV	Rudell, Jacques C., University of California, Berkeley, "CMOS VLSI Frequency Translation for Multi-Standard Wireless Communication Transceivers," Qualifying Examination, April 8, 1996, pp. 1-24.
QV	Chang, Glenn et al., "An Agile, Low-Power CMOS Digitally Synthesized 0-13 MHz Sinewave Generator," Integrated Circuits & Systems Laboratory, Electrical Engineering Department, University of California, Los Angeles, (14 pages).

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